

Docket: CS 01 - 214
S/N: 10/662,674



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Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

George O. Saile, Reg. No. 19,572
28 Davis Avenue
Poughkeepsie, N.Y. 12603

Subject:

Serial No.: 10/662,674	Filed: 09/15/2003
Inventor: Yong Meng Lee	
Title: Method of Forming Double-Gated Silicon-on-Insulator (SOI) Channel Transistors With Corner Rounding	
Group Art Unit: TBD	Examiner: TBD
Attorney Docket: CS 01 - 214	

RESPONSE TO NOTICE TO FILE CORRECTED APPLICATION PAPERS

Dear Sir:

In response to the Notice to File Corrected Application Papers dated 12/05/2003 (copy enclosed herewith), please consider the following remarks:

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450, on _____

February 5, 2004.

Signature/Date

2/5/04

Stephen B. Ackerman

Reg. No. 37,761

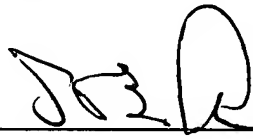
Charge to Deposit Account

The Commissioner is hereby authorized to charge payment of the fee of \$ 0 associated with this communication, or credit any overpayment, to Deposit Account No. 19-0033. The Commissioner is also authorized to charge any additional fee under 37 CFR §1.16 and 1.17 to this Deposit Account. A duplicate copy of this sheet is enclosed. The Commissioner is hereby authorized to charge payment of any additional fees involved with added Claims and the like to Deposit Account No. 19-0033.

2/5/04

Date

Respectively submitted,



George O. Saile; or

Stephen B. Ackerman